



# IEEE-EDS GUEST SPEAKER

**“A 1-mW 12 Gbps adaptive passive equalizer in 90-nm CMOS”**

**By Dr. Patrick Yue**

***Department of Electrical and Computer Engineering  
University of California, Santa Barbara***

Hosted by IEEE Electron Devices Society Student Chapter at BCOE

**DATE: Friday, Oct 9<sup>th</sup>, 2009 TIME: 2:00 – 3:00 PM**

**LOCATION: Engineering Building Unit II (EBUII) 202**

Prof. Patrick Yue received his Ph.D. in EE from Stanford in 1998. He is currently an Associate Professor in ECE Department and the Associate Director of Computer Engineering Program at the University of California Santa Barbara. He is an Adjunct Professor in the ECE Department of Carnegie Mellon University, Pittsburgh, PA and in the Institute of Microelectronics at the Chinese Academy of Sciences, Beijing, China. His current interests include high-speed CMOS wireless and wireline IC design, cell-based RF/mm-wave modeling and design methodology, and integrated biomedical sensors. From 2003-2006, he was with Carnegie Mellon University as an Assistant Professor in ECE. From 1998-2002, he co-founded Atheros Communications and contributed to the development of the first 802.11a CMOS RF transceiver for high-volume production which ignited the proliferation of Wi-Fi technology. While working in the industry, Prof. Yue held the position of a Consulting Assistant Professor in Electrical Engineering at Stanford University between 2001 and 2003. Prof. Yue remains active in consulting for IC design companies and currently serves on the advisory boards of several semiconductor startups in the U.S. and China. Prof. Yue has contributed to more than fifty peer-reviewed technical papers and two book chapters. He currently holds thirteen U.S. patents, majority of which are utilized in commercial products. He was the co-recipient of the 2003 International Solid-State Circuits Conference (ISSCC) Best Student Paper Award. His 1998 paper “On-chip spiral inductors with patterned ground shields for Si-based RF ICs” is among the most cited articles in the history of the IEEE Journal of Solid-State Circuits according Thomson ISI. He has served on the technical program committees of the IEEE RFIC Symposium, A-SSCC, VLSI-DAT, ISLPED, and CSICS. He has been a member of the IEEE Electron Devices Society VLSI Technology and Circuits Committee and a Senior Member of IEEE since 2005.



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