Process/Thread/Task Scheduling

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Recap: Each process has a separate virtual memory space





Virtually, every process seems to have a processor, but only a few of them are physically executing.

Recap: Threads



Recap: Solving the "Critical Section Problem"

- Mutual exclusion at most one process/thread in its critical 1. section
- 2. Progress/Fairness a thread outside of its critical section cannot block another thread from entering its critical section 3. Progress/Fairness — a thread cannot be postponed
- indefinitely from entering its critical section
- 4. Accommodate nondeterminism the solution should work regardless the speed of executing threads and the number of processors

Bounded-Buffer Problem

- Also referred to as "producer-consumer" problem
- Producer places items in shared buffer
- Consumer removes items from shared buffer





Without synchronization, you may write

int buffer[BUFF_SIZE]; // shared global

```
int main(int argc, char *argv[]) {
    pthread_t p;
    printf("parent: begin\n");
                                              void *child(void *arg) {
    // init here
                                                  int out = 0;
                                                  printf("child\n");
    Pthread_create(&p, NULL, child, NULL);
    int in = 0;
                                                  while(TRUE) {
    while(TRUE) {
                                                      int item = buffer[out];
       int item = \dots;
                                                      out = (out + 1) \%
       buffer[in] = item;
                                              BUFF_SIZE;
       in = (in + 1) \% BUFF_SIZE;
                                                      // do something w/ item
    printf("parent: end\n");
                                                  return NULL;
    return 0;
                                              }
}
```



Use locks

	int buffer[BUFF_SIZE volatile unsigned in
<pre>Int main(int argc, char *argv[]) { pthread_t p; printf("parent: begin\n"); // init here Pthread_create(&p, NULL, child, NULL int in = 0; while(TRUE) { int item =; Pthread_mutex_lock(&lock); buffer[in] = item; in = (in + 1) % BUFF_SIZE; Pthread_mutex_unlock(&lock); } printf("parent: end\n"); return 0; }</pre>	<pre>.); void *child(void int out = 0; printf("chil while(TRUE) Pthread_ int item out = (o Pthread_ // do so } return NULL;</pre>

You cannot produce and consume simultaneously!

]; // shared global t lock = 0;

1 *arg) {
.d\n");
{
.mutex_lock(&lock);
n = buffer[out];
out + 1) % BUFF_SIZE;
.mutex_unlock(&lock);
omething w/ item

Semaphores

Semaphores

- A synchronization variable
- Has an integer value current value dictates if thread/process can proceed
- Access granted if val > 0, blocked if val == 0
- Maintain a list of waiting processes



Semaphore Operations

- sem_wait(S)
 - if S > 0, thread/process proceeds and decrement S
 - if S == 0, thread goes into "waiting" state and placed in a special queue
- sem_post(S)
 - if no one waiting for entry (i.e. waiting queue is empty), increment S
 - otherwise, allow one thread in gueue to proceed



Semaphore Op Implementations



```
sem_post(sem_t *s) {
    s->value++;
    wake_one_waiting_thread(); // if there is one
}
```



Atomicity in Semaphore Ops

- Semaphore operations must operate atomically
 - Requires lower-level synchronization methods requires (test-andset, etc.)
 - Most implementations still require on busy waiting in spinlocks
- What did we gain by using semaphores?
 - Easier for programmers
 - Busy waiting time is limited



Using semaphores

• What variables to use for this problem?

C

filled

```
int main(int argc, char *argv[]) {
    pthread_t p;
    printf("parent: begin\n");
    // init here
    Pthread_create(&p, NULL, child, NULL);
    int in = 0;
    Sem_init(&filled, 0);
    Sem_init(&empty, BUFF_SIZE);
    while(TRUE) {
       int item = ...;
       Sem_wait(&W);
       buffer[in] = item;
       in = (in + 1) \% BUFF_SIZE;
       Sem_post(&X);
                                                 }
    }
    printf("parent: end\n");
                                              W
    return 0;
                                  Α
                                            empty
}
                                  В
                                            empty
```

int buffer[BUFF_SIZE]; // shared global sem_t filled, empty;

```
void *child(void *arg) {
    int out = 0;
    printf("child\n");
    while(TRUE) {
        Sem_wait(&Y);
        int item = buffer[out];
        // do something w/ item
        Sem_post(&Z);
    return NULL;
```

X

empty

filled

empty



Outline

- Mechanisms of changing processes
- Basic scheduling policies
- An experimental time-sharing system The Multi-Level Scheduling Algorithm
- Scheduler Activations

The mechanisms of changing processes

The mechanisms of changing the running processes

- Cooperative Multitasking (non-preemptive multitasking)
- Preemptive Multitasking



Cooperative multitasking

- How many of the following statements about cooperative multitasking is/are correct?
 - The OS can change the running process if the current process give up the resource (1)
 - The OS can change the running process if the current process traps into OS kernel 2
 - ③ The OS can change the running process if the current process raise an exception
 - like divide by zero Anytime if we make a system call to the OS, the OS can potentially switch a proces ④ The OS can actively change the running process if the current process is running for Unfortunately, the OS cannot — if the process never traps a long enough time
 - A. 0
 - B. 1
 - C. 2

D. 3

Cooperative multitasking — processes voluntarily yield control periodically or when idle in order to enable multiple applications to be run simultaneously



Preemptive Multitasking

- The OS controls the scheduling can change the running process even though the process does not give up the resource
- But how?



How to achieve preemptive multitasking

- Which of the following mechanism are used to support preemptive multitasking?
 - A. Exception
 - B. Interrupt
 - C. System calls



Three ways to invoke OS handlers

- System calls / trap instructions raised by applications
 - Display images, play sounds
- Exceptions raised by processor itself
 - Divided by zero, unknown memory addresses
- Interrupts raised by hardware
 - Keystroke, network packets



0x1bad(%eax).%dh %al (%eax 0x1010 -0x2bb84(%ebx).%ea> %eax,-0x2bb8a(%ebx) -0x2bb8c(%ebx) -0x2bf3d(%ebx),%eax



kernel/privilegee





How preemptive multitasking works

- Setup a **timer** (a hardware feature by the processor) event before the process start running
- After a certain period of time, the timer generates interrupt to force the running process transfer the control to OS kernel
- The OS kernel code decides if the system wants to continue the current process
 - If not context switch
 - If yes, return to the process



Scheduling Policies from Undergraduate OS classes

CPU Scheduling

- Virtualizing the processor
 - Multiple processes need to share a single processor
 - Create an illusion that the processor is serving my task by rapidly switching the running process
- Determine which process gets the processor for how long

What you learned before

- Non-preemptive/cooperative: the task runs until it finished
 - FIFO/FCFS: First In First Out / First Come First Serve
 - SJF: Shortest Job First
- Preemptive: the OS periodically checks the status of processes and can potentially change the running process
 - STCF: Shortest Time-to-Completion First
 - RR: Round robin



Best for turn-around time

Assume that we have the following 3 processes

	Arrival time	Task leng
P1	0	5
P2	1	4
P3	4	1

which of the following scheduling policy yields the best average turn-around time? (assume we prefer not to switch process if two process have the same criteria)



41



Ith

(5-0)+(9-1)+(10-4)=6.33	P3		2	Ρ	
		10	89	,	
(5-0)+(6-4)+(10-1)=5.33		2	P2		
		10	89	,	
(5-0)+(6-4)+(10-1)=5.33	P2				
		10	89	'	
(10-0)+(9-1)+(5-4)=6.33	P1	P2	P1	2	
		10	89	,	

Parameters for policies

- How many of the following scheduling policies require knowledge of process run times?
 - ① FIFO/FCFS: First In First Out / First Come First Serve
 - SJF: Shortest Job First
 - STCF: Shortest Time-to-Completion First forget about them in real implementation
 - ④ RR: Round robin
 - A. 0
 - **B**. 1

 - D. 3
 - E. 4



These policies are not realistic

An experimental time-sharing system

Fernando J. Corbató, Marjorie Merwin-Daggett and Robert C. Daley Massachusetts Institute of Technology, Cambridge, Massachusetts

ng system Robert C. Daley Massachusetts

Why Multi-level scheduling algorithm

- Why MIT's experimental time-sharing system proposes Multi-level schedule algorithm? How many of the following
 - Optimize for the average response time of tasks
 - Optimize for the average turn-around time of tasks (2)
 - Optimize for the performance of long running tasks (3)
 - Guarantee the fairness among tasks (4)

4. The response time for programs of equal size, entering the system at the same time, and being run for multiple quanta, is no worse than approximately twice the response-time occurring in a single quanta round-robin procedure. If

B. 1 C. 2 D. 3 E. 4

A. 0

To illustrate the strategy that can be employed to improve the saturation performance of a time-sharing system, a multi-level scheduling algorithm is presented. This algorithm also

> Several important conclusions can be drawn from the above algorithm which allow the performance of the system to be bounded.

Why Multi-level scheduling algorithm?

- System saturation the demand of computing is larger than the physical **processor** resource available
- Service level degrades
 - Lots of program swap ins-and-outs (known as context switches) in our current terminology)
 - User interface response time is bad Service — you have to wait until your turn
 - Long running tasks cannot make good progress — frequent swap in-and-out



Service vs. Number of Active Users

Context Switch Overhead

You think round robin should act like this —



But the fact is —

		P1	Overhe P1 ->	ead P2	P2	Overhea P2 -> P	ad '3	P3	Overhead P3 -> P1		P1	Overhea P1 -> P2	id 2	P2
0	1		1	2		2	3		3	4		4	5	

- Your processor utilization can be very low if you switch frequently
- •No process can make sufficient amount of progress within a given period of time
- It also takes a while to reach your turn



Overhead P2 -> P3

The Multilevel Scheduling Algorithm

- Place new process in the one of the queue
 - Depending on the program size

$$l_{o} = \left[\log_{2} \left(\left[\frac{w_{p}}{w_{q}} \right] + 1 \right) \right] \qquad w_{p} \text{ is the program memory size} - s assigned to lower numbered}$$

- Smaller tasks are given higher priority in the beginning
- Schedule processes in one of N queues
 - Start in initially assigned queue *n*
 - Run for 2ⁿ quanta (where n is current depth)
 - If not complete, move to a higher queue (e.g. n + 1)
 - Larger process will execute longer before switch •
- Level *m* is run only when levels 0 to m-1 are empty
- Smaller process, newer process are given higher priority



smaller ones are d queues Why?

The Multilevel Scheduling Algorithm

- Not optimized for anything it's never possible to have an optimized scheduling algorithm without prior knowledge regarding all running processes
- It's practical many scheduling algorithms used in modern OSes still follow the same idea

Correction: disable interrupts?

- Which of the following can disabling interrupts guarantee on multicore processors?
 - A. At most one process/thread in its critical section
 - B. A thread outside of its critical section cannot block another thread from entering its critical section
 - C. A thread cannot be postponed indefinitely from entering its critical section What if the thread hold the critical section crashes?
 - D. The solution should work regardless the speed of executing threads and the number of processors
 - E. None of the above



What if we have multiple processors?

Announcement

- Reading quiz due next Tuesday
- Project due date 3/3
 - In about a month
 - Please come to our office hours if you need help for your projects