Optimizing Our Design!

Prof. Usagi



Q Search...

Home

How do I ask and answer homework questions?

Questions

Asked 11 years, 5 months ago Active 4 months ago Viewed 50k times

Tage

The guidelines outlined below are rooted in two principles:

- It is okay to ask about homework. For one, it would wanted to. Stack Overflow exists to help programmers for programming problems, both simple and complex,
- Providing an answer that doesn't help a student to interest. Therefore you might choose to treat homework questions.

Asking about homework

- Make a good faith attempt to solve the problem your part your question will likely be booed off the:
- Ask about specific problems with your existing im try some more of your own work first or searching for likely to be a better resource at this stage than Stack (
- Be aware of school policy. If your school has a polic make sure you are aware of it before you ask for / rec specific restrictions (for example, you can receive help them in the question so that those providing assistance
- Never use code you don't understand. It definitely vassignments, on tests, etc.) and it could be, at best, vexplain code you turned in.
- Understand the difference between "asking a question about your homework" and
 "asking a specific question about the code in your homework". You should never ask a
 question about your homework because more often than not it will not meet the
 recommendations in the rest of this question. Instead, ask the question about the code you
 wrote to solve your homework problem, and be specific with the inputs, desired outputs, and
 error messages. It is ideal if you take your code and create an MCVE instead of pasting your
 entire code, especially if it is a long code block.



stack overflow

nework questions

d the asker in the correct direction. Genuine ents, but trying to provide that is usually appreciated for

plete code sample if you believe it would not help the u can use pseudo-code first, and, in the spirit of creating ne back after a suitable amount of time and edit your de. This way, the student still has to write their own vailable after the assignment has ended.

nclude artificial constraints, and honor those constraints. nay affect whether or not a question should be closed as

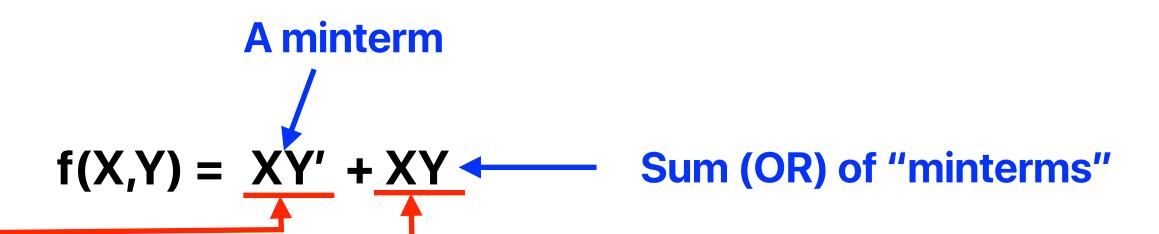
mework questions in good faith, even if they break ould merit downvotes even if the question weren't ious at first glance that a question is homework, o see it here. It is a good idea to suggest editing the

naven't yet learned something obvious or developed the oned programmer. Do add a *respectful* comment or practices and better style.

- Don't downvote a homework question that follows the guidelines and was asked in good faith.
- It's okay to ask if a question is homework, but be polite.
- As with homework questions, other questions along the lines of "plz send teh codez" may be
 closed as "too broad". Use your best judgment. Remember: students are new programmers
 and often do not yet understand what is expected of them on the site. We should politely and
 patiently help them gain that understanding.

Recap: Canonical form — Sum of "Minterms"

| Inp | out | Output |
|-----|-----|--------|
| X | Y | Output |
| 0 | 0 | 0 |
| 0 | 1 | 0 |
| 1 | 0 | 1 |
| 1 | 1 | 1 |



XNOR

| Ing | out | Output | £(A D) A(D) - AD |
|-----|-----|--------|--------------------|
| A | В | Output | f(A,B) = A'B' + AB |
| 0 | 0 | 1 | |
| 0 | 1 | 0 | |
| 1 | 0 | 0 | |
| 1 | 1 | 1 | 3 |

Recap: Canonical form — Product of "Maxterms" A "maxterm

| Input | | Output | |
|-------|---|--------|--|
| X | Y | Output | |
| 0 | 0 | 0 | |
| 0 | 1 | 0 | |
| 1 | 0 | 1 | |
| 1 | 1 | 1 | |

XNOR

| Output | Input | | |
|--------|-------|-----|---|
| Output | В | A B | |
| 1 | 0 | 0 | |
| 0 | 1 | 0 | Ц |
| 0 | 0 | 1 | |
| 1 | 1 | 1 | |

Recap: The Adder

```
module FA( input a,
                                    A_0B_0
             A_2B_2
                                                            input b,
 A_3B_3
                        A_1B_1
                                                            input cin,
                                                            output cout,
                                                            output out );
                                                   assign out = (~a\&b\&~cin)|(a\&~b\&~cin)|(~a\&~b\&cin)|(a\&b\&cin);
                                      Half
              Full
                          Full
                                                   assign cout = (a\&b\&\sim cin) | (\sim a\&b\&cin) | (a\&\sim b\&cin) | (a\&b\&cin);;
   Full
                                                   endmodule
             Adder
                         Adder
                                     Adder
  Adder
                                                   module HA input a,
                                                            input b,
                                                            output cout,
                  O_2
                              O<sub>1</sub>
                                         Oo
                                                            output out );
                                                   assign out = (~a \& b)|(a \& ~b);
                                                   assign cout = a\&b;
                                                   endmodule
module adder( input[3:0] A;
                 input[3:0] B,
                                             Connecting ports by name yields
                 output[3:0] 0,
                                               clearer and less buggy code.
                 output cout);
wire [2:0] carries;
HA ha0(.a(A[0]), .b(B[0]), .out(O[0]), .cout(carries[0]));
FA fa1(.a(A[1]), .b(B[1]), .cin(carries[0]), .out(O[1]), .cout(carries[1]));
FA fa2(.a(A[2]), .b(B[2]), .cin(carries[1]), .out(O[2]), .cout(carries[2]));
FA fa3(.a(A[3]), .b(B[3]), .cin(carries[2]), .out(O[2]), .cout(cout);
endmodule
```

Recap: Testing the adder!

```
`timescale 1ns/1ns // Add this to the top of your file to set time scale
module testbench();
reg [3:0] A, B;
reg C0;
wire [3:0] S;
wire C4;
adder uut (.B(B), .A(A), .sum(S), .cout(C4)); // instantiate adder
initial
begin
A = 4'd0; B = 4'd0; C0 = 1'b0;
\#50 A = 4'd3; B = 4'd4; // wait 50 ns before next assignment
\#50 A = 4'b0001; B = 4'b0010; // don't use \#n outside of testbenches
end
endmodule
```

Recap: You can also use only NANDs

Now, only 5 gates and 4 transistors each — 20 transistors!

Outline

- Simplifying design using "theorems"
- Karnaugh maps

Can we simplify these functions?

Laws in Boolean Algebra

| | OR | AND |
|-------------------|---------------------|------------------|
| Associative laws | (a+b)+c=a+(b+c) | (a⋅b) ⋅c=a⋅(b⋅c) |
| Commutative laws | a+b=b+a | a·b=b·a |
| Distributive laws | a+(b·c)=(a+b)·(a+c) | a·(b+c)=a·b+a·c |
| Identity laws | a+0=a | a·1=a |
| Complement laws | a+a'=1 | a·a′=0 |

Duality: We swap all operators between (+,.) and interchange all elements between (0,1). For a theorem if the statement can be proven with the laws of Boolean algebra, then the duality of the statement is also true.

Some more tools

| | OR | AND |
|------------------------|--------------------|--|
| DeMorgan's Theorem | (a + b)' = a'b' | a'b' = (a + b)' |
| Covering Theorem | a(a+b) = a+ab = a | ab + ab' = (a+b)(a+b') = a |
| Consensus Theorem | ab+ac+b'c = ab+b'c | (a+b)(a+c)(b'+c) = (a+b)(b'+c) |
| Uniting Theorem | a (b + b') = a | (a+b)·(a+b')=a |
| Shannon's Expansion | | = a'b' + bc + ab'c f(1, b, c) + a' f(0,b,c) |

Applying Theorems

Which of the following represents CB+BA+C'A?

A. AB+AC'

B. BC+AC'

C. AB+BC

D. AB+AC

E. None of the above

| | OR | |
|---------------------|---------------------|---|
| Associative laws | (a+b)+c=a+(b+c) | (a·b) · |
| Commutative laws | a+b=b+a | a. |
| Distributive laws | a+(b·c)=(a+b)·(a+c) | a∙(b+c |
| Identity laws | a+0=a | a |
| Complement laws | a+a'=1 | а |
| DeMorgan's Theorem | (a + b)' = a'b' | a'b' = |
| Covering Theorem | a(a+b) = a+ab = a | ab + ab' = (|
| Consensus Theorem | ab+ac+b'c = ab+b'c | (a+b)(a+c)(b' |
| Uniting Theorem | a (b + b') = a | (a+b) |
| Shannon's Expansion | | a'b' + bc + ab'c , b, c) + a' f(0,b,c) |

AND

 $(a \cdot b) \cdot c = a \cdot (b \cdot c)$

a·b=b·a

 $a \cdot (b+c) = a \cdot b + a \cdot c$

a-1=a

a⋅a′=0

a'b' = (a + b)'

ab + ab' = (a+b)(a+b') = a

(a+b)(a+c)(b'+c) = (a+b)(b'+c)

 $(a+b)\cdot(a+b')=a$

Applying Theorems

Which of the following represents CB+BA+C'A?

A. AB+AC'

Consensus Theorem

ab+ac+b'c = ab+b'c

- B. BC+AC'
- C. AB+BC
- D. AB+AC
- E. None of the above

```
CB + BA1 + C'A
```

$$= BC + BA (C'+C) + C'A$$

$$= BC + BAC' + BAC + C'A$$

$$= (1+A)BC + (B+1)AC'$$

$$= BC + AC'$$



How many "OR"s?

14

For the truth table shown on the right, what's the minimum

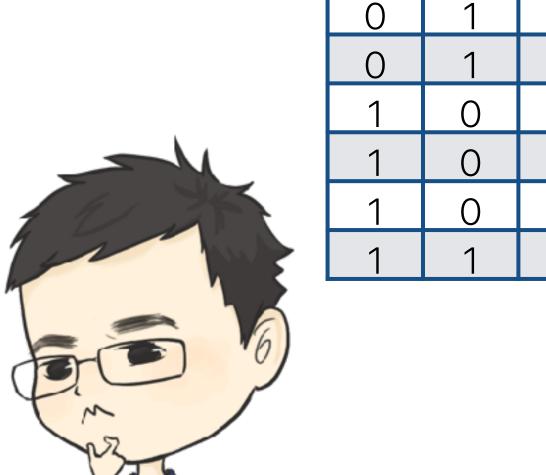
number of "OR" gates we need?

| Δ | | 1 |
|---|---|---|
| | • | 1 |

B. 2

C. 3

E. 5





Input

0

A

0

0

C

Output

How many "OR"s?

 For the truth table shown on the right, what's the minimum number of "OR" gates we need?

```
A. 1 F(A,B,C) = Uniting Theorem

B. 2 A'B'C'+ A'B'C+ A'BC' + A'BC + AB'C' + ABC'

C. 3 = A'B'(C'+C)+ A'B(C'+C)+ AC'(B'+B)

D. 4 = A'B' + A'B + AC'

E. 5 = A' + AC' = A'(1+C')+AC' Distributive Laws

= A' + A'C' + AC'
= A' + (A'+A)C'
= A' + C'
```

| Input C | | | Output |
|---------|---|---|--------|
| 0 | 0 | 0 | 1 |
| 0 | 0 | 1 | 1 |
| 0 | 1 | 0 | 1 |
| 0 | 1 | 1 | 1 |
| 1 | 0 | 0 | 1 |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 1 |
| 1 | 1 | 1 | 0 |

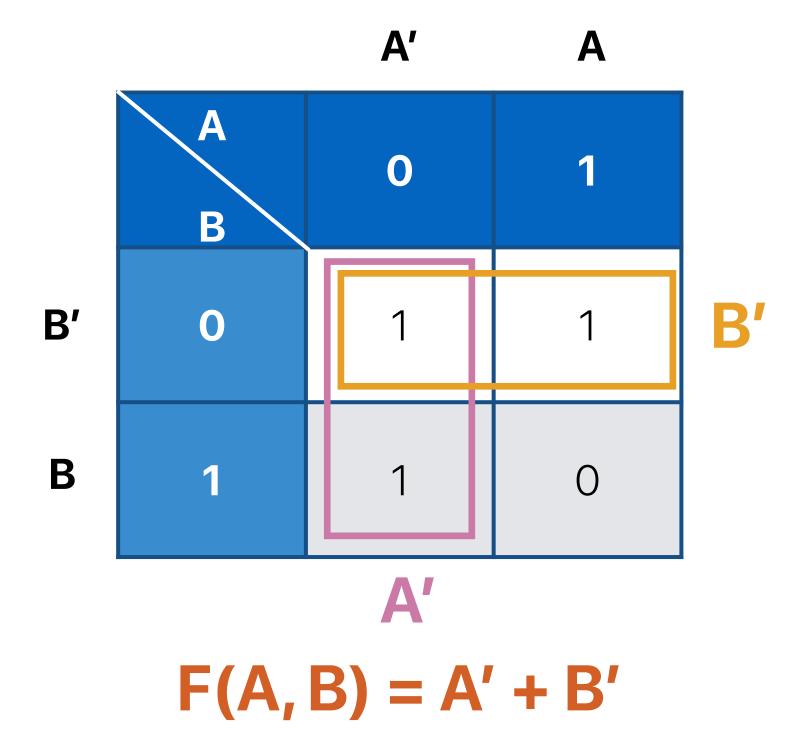
Karnaugh maps

Karnaugh maps

- Alternative to truth-tables to help visualize adjacencies
- Guide to applying the uniting theorem
- Steps
 - Create a 2-D truth table with input variables on each dimension, and adjacent column(j)/row(i) only change one bit in the variable.
 - Fill each (i,j) with the corresponding result in the truth table
 - Identify ON-set (all 1s) with size of power of 2 (i.e., 1, 2, 4, 8, ...) and "unite" them terms together (i.e. finding the "common literals" in their minterms)
 - Find the "minimum cover" that covers all 1s in the graph
 - Sum with the united product terms of all minimum cover ON-sets

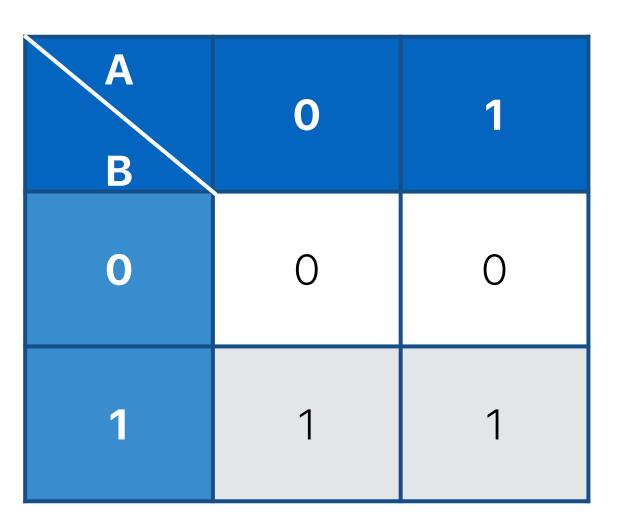
2-variable K-map example

| Inp | Output | |
|-----|--------|--------|
| Α | В | Output |
| 0 | 0 | 1 |
| 0 | 1 | 1 |
| 1 | 0 | 1 |
| 1 | 1 | 0 |



Practicing 2-variable K-map

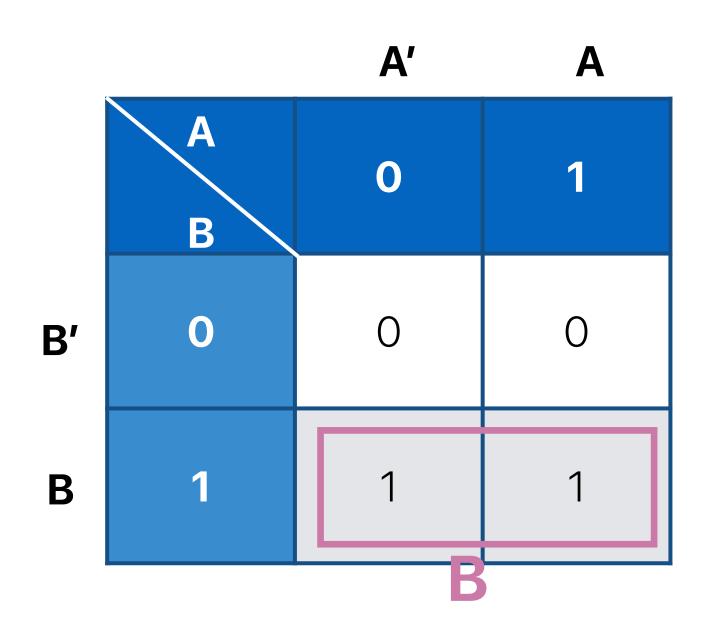
- What's the simplified function of the following K-map?
 - A. A'
 - B. A'B
 - C. AB'
 - D. B
 - E. A



Practicing 2-variable K-map

What's the simplified function of the following K-map?

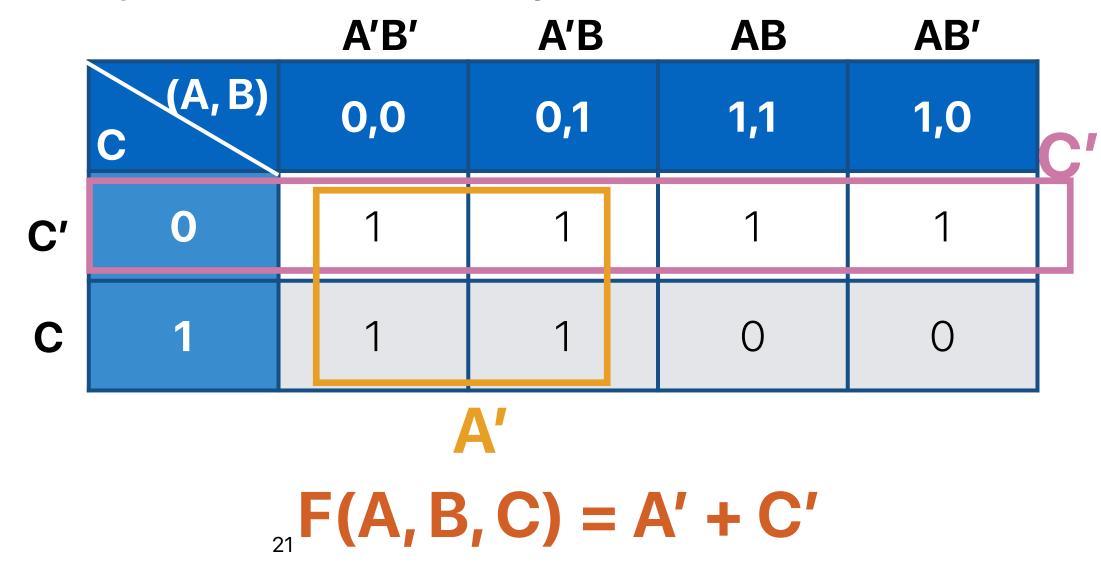
- A. A'
- B. A'B
- C. AB'
- D. B
 - E. A



3-variable K-map?

- Reduce to 2-variable K-map 1 dimension will represent two variables
- Adjacent points should differ by only 1 bit
 - So we only change one variable in the neighboring column
 - 00, 01, 11, 10 such numbering scheme is so-called **Gray-code**

| | Input | Output | |
|---|-------|--------|--------|
| Α | В | C | Output |
| 0 | 0 | 0 | 1 |
| 0 | 0 | 1 | 1 |
| 0 | 1 | 0 | 1 |
| 0 | 1 | 1 | 1 |
| 1 | 0 | 0 | 1 |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 1 |
| 1 | 1 | 1 | 0 |



Minimum number of SOP terms to cover the following

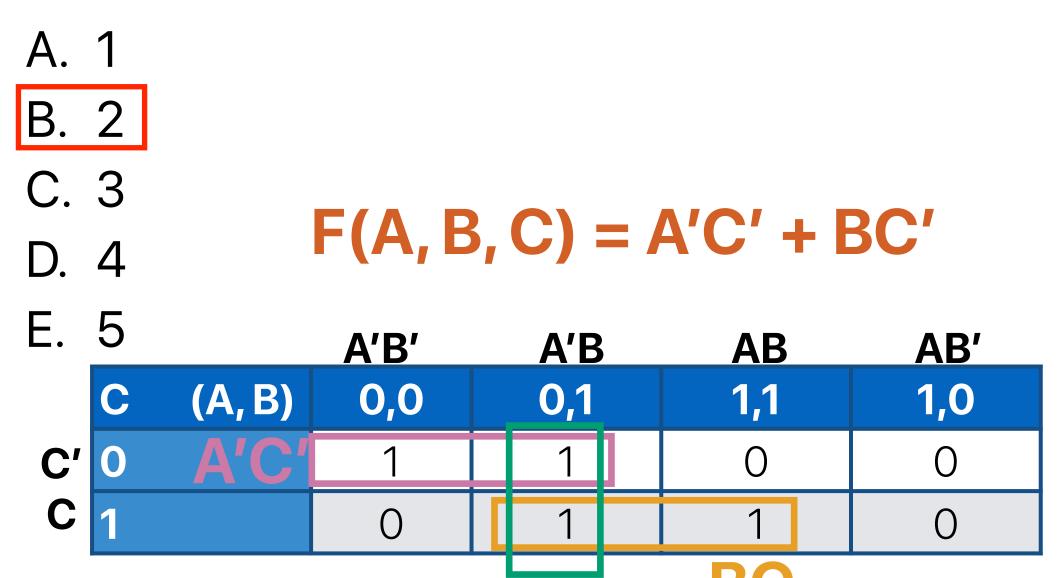
function?

- A. 1
- B. 2
- C. 3
- D. 4
- E. 5

| | Input | Output | |
|---|-------|--------|--------|
| Α | В | C | Output |
| 0 | 0 | 0 | 1 |
| 0 | 0 | 1 | 0 |
| 0 | 1 | 0 | 1 |
| 0 | 1 | 1 | 1 |
| 1 | 0 | 0 | 0 |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 0 |
| 1 | 1 | 1 | 1 |

Minimum number of SOP terms to cover the following

function?



| | Input | Output | |
|---|-------|--------|--------|
| Α | ВС | | Output |
| 0 | 0 | 0 | 1 |
| 0 | 0 | 1 | 0 |
| 0 | 1 | 0 | 1 |
| 0 | 1 | 1 | 1 |
| 1 | 0 | 0 | 0 |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 0 |
| 1 | 1 | 1 | 1 |

Minimum number of SOP terms to cover the following

function?

- A. 1
- B. 2
- C. 3
- D. 4
- E. 5

| | Input | Output | |
|---|-------|--------|--------|
| Α | В | C | Output |
| 0 | 0 | 0 | 1 |
| 0 | 0 | 1 | 1 |
| 0 | 1 | 0 | 0 |
| 0 | 1 | 1 | 0 |
| 1 | 0 | 0 | 1 |
| 1 | 0 | 1 | 1 |
| 1 | 1 | 0 | 0 |
| 1 | 1 | 1 | 0 |

Minimum number of SOP terms to cover the following

function?

| | A. | 1 |
|--|----|---|
|--|----|---|

B. 2

C. 3

D. 4

E. 5

| Output | Input | | | |
|--------|-------|---|---|--|
| Output | C | В | Α | |
| 1 | 0 | 0 | 0 | |
| 1 | 1 | 0 | 0 | |
| 0 | 0 | 1 | 0 | |
| 0 | 1 | 1 | 0 | |
| 1 | 0 | 0 | 1 | |
| 1 | 1 | 0 | 1 | |
| 0 | 0 | 1 | 1 | |
| 0 | 1 | 1 | 1 | |
| | | | | |

A'B'

AR'

| | | | AD | AD | | | \mathcal{L} | |
|----|---|--------|-----|-----|-----|-----|---------------|--|
| | C | (A, B) | 0,0 | 0,1 | 1,1 | 1,0 | 0,0 | |
| C' | 0 | | 1 | 0 | 0 | 1 | 1 | |
| C | 1 | | 1 | 0 | 0 | 1 | 1 | |

Δ'R

A'R'

ΔR

Minimum SOP for a full adder

- Minimum number of SOP terms to cover the "Out" function for a one-bit full adder?
 - A. 1
 - B. 2
 - C. 3
 - D. 4
 - E. 5

| Input | | | Output | | |
|-------|---|-----|--------|------|--|
| A | В | Cin | Out | Cout | |
| 0 | 0 | 0 | 0 | 0 | |
| 0 | 1 | 0 | 1 | 0 | |
| 1 | 0 | 0 | 1 | 0 | |
| 1 | 1 | 0 | 0 | 1 | |
| 0 | 0 | 1 | 1 | 0 | |
| 0 | 1 | 1 | 0 | 1 | |
| 1 | 0 | 1 | 0 | 1 | |
| 1 | 1 | 1 | 1 | 1 | |

Minimum SOP for a full adder

 Minimum number of SOP terms to cover the "Out" function for a one-bit full adder?

A. 1

B. 2

C. 3

D. 4

E. 5

Cin' Cin

| | AB | AB | AB | AB |
|-----------|-----|-----|-----|-----|
| Out(A, B) | 0,0 | 0,1 | 1,1 | 1,0 |
| 0 | 0 | 1 | O | 1 |
| 1 | 1 | 0 | 1 | 0 |

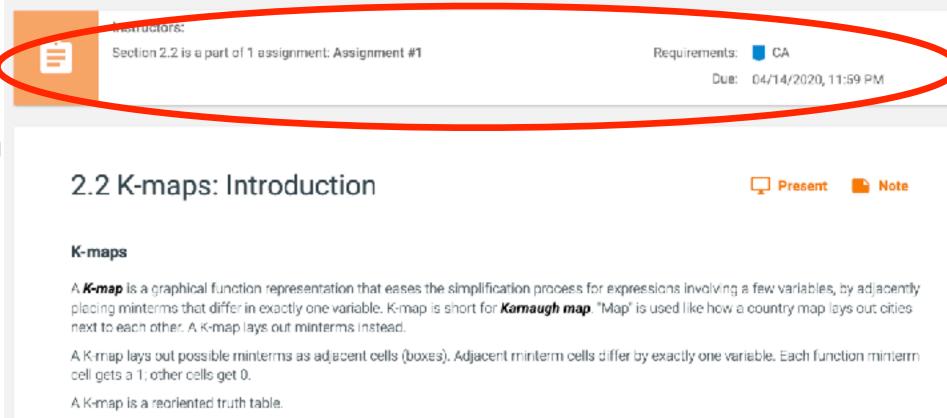
A/D/

| Input | | | Output | | |
|-------|---|-----|--------|------|--|
| A | В | Cin | Out | Cout | |
| 0 | 0 | 0 | 0 | 0 | |
| 0 | 1 | 0 | 1 | 0 | |
| 1 | 0 | 0 | 1 | 0 | |
| 1 | 1 | 0 | 0 | 1 | |
| 0 | 0 | 1 | 1 | 0 | |
| 0 | 1 | 1 | 0 | 1 | |
| 1 | 0 | 1 | 0 | 1 | |
| 1 | 1 | 1 | 1 | 1 | |

ADI

Announcement

- Lab 1 due tonight
 - Submit through iLearn > Labs
- Reading quiz 3 due 4/14 BEFORE the lecture
 - Under iLearn > reading quizzes
- Assignment 1 also due 4/14
 - Submit on zyBooks.com directly all challenge questions up to 2.2
- Lab 2 due 4/16
 - Watch the video and read the instruction **BEFORE** your session
 - There are links on both course webpage and iLearn lab section
 - Submit through iLearn > Labs



Electrical Computer Science Engineering 120A

